

**UNITED STATES INTERNATIONAL TRADE COMMISSION**  
**Washington, D.C.**

**In the Matter of**

**CERTAIN LARGE SCALE INTEGRATED  
CIRCUIT SEMICONDUCTOR CHIPS AND  
PRODUCTS CONTAINING SAME**

**Inv. No. 337-TA-716**

**Order No. 5: Setting Procedural Schedule And Granting, In Part, A Joint Motion**


This investigation shall be conducted in accordance with the following schedule:

First settlement conference by	July 2, 2010
Joint report regarding first settlement conference	July 7, 2010
File identification of expert witnesses, including expertise and curriculum vitae	August 20, 2010
File list of tentative witnesses a party will call to testify at the hearing, with an identification of each witness' relationship to the party	August 26, 2010
File notice of prior art	September 24, 2010
Second settlement conference by	October 8, 2010
Joint report regarding second settlement conference	October 13, 2010
Fact discovery cutoff	November 19, 2010
Deadline for motions to compel discovery	November 23, 2010
Exchange of initial expert reports on issues for which party bears burden of proof	December 1, 2010
Third settlement conference by	December 10, 2010
Deadline for filing motions for summary determination	December 10, 2010
Joint report regarding third settlement conference	December 15, 2010

Exchange of expert rebuttal reports	December 15, 2010
Expert discovery cutoff	December 22, 2010
Complainant and Respondents submit direct exhibits	January 7, 2011
Submit and serve proposed deposition designations	January 7, 2011
Staff submits direct exhibits	January 12, 2011
Objections to direct exhibits and deposition designations	January 14, 2011
All parties submit and serve rebuttal exhibits	January 14, 2011
Objections to rebuttal exhibits	January 21, 2011
All parties file prehearing briefs	January 26, 2011
Motions <i>in limine</i> due	February 4, 2011
Responses to motions <i>in limine</i> due	February 8, 2011
Prehearing conference	February 10, 2011
Hearing in Hearing Room 100(A)	February 17 - 25, 2011
Posthearing briefs due	March 15, 2011
Replies due	March 22, 2011

In view of the above schedule, the joint motion of Complainant and Respondents proposing a procedural schedule (Motion No. 716-2) is granted, in part.

**So Ordered.**

  
 Carl C. Charneski  
 Administrative Law Judge


Issued: June 3, 2010

**CERTAIN LARGE SCALE INTEGRATED CIRCUIT SEMICONDUCTOR CHIPS AND PRODUCTS CONTAINING SAME**

337-TA-716

**PUBLIC CERTIFICATE OF SERVICE**

I, Marilyn R. Abbott, hereby certify that the attached **ORDER NO. 5**, has been served by hand upon the Commission Investigative Attorney Stephen R. Smith, Esq., and the following parties as indicated, on JUN - 3 2010.

  
Marilyn R. Abbott, Secretary  
U.S. International Trade Commission  
500 E Street, SW, Room 112A  
Washington, D.C. 20436

**FOR COMPLAINANTS PANASONIC CORP.:**

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**FOR RESPONDENTS FREESCALE SEMICONDUCTOR JAPAN, LTD., FREESCALE SEMICONDUCTOR MALAYSIA SDN. BHD., FREESCALE SEMICONDUCTOR PTE. LTD., FREESCALE SEMICONDUCTOR TAIWAN LTD., FREESCALE SEMICONDUCTOR, INC., MOUSER ELECTRONICS, INC., MOTOROLA INC., AND PREMIER FARNELL CORP., d/b/a/ NEWARK:**

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**CERTAIN LARGE SCALE INTEGRATED CIRCUIT SEMICONDUCTOR CHIPS AND PRODUCTS CONTAINING SAME**

337-TA-716

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